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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 10/085,632 02/27/2002 PAT 6,639,287 *PK* ✓

** FOREIGN APPLICATIONS *****
 JAPAN JP 2001-052099 02/27/2001 *PK* ✓

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>[Signature]</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 27	TOTAL CLAIMS 2	INDEPENDENT CLAIMS 1
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TITLE
 METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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